

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of
Invention

Peripheral Device Feature Allowing Processors to Enter A Low
Power State

Application Number : 10/762767



Confirmation Number: 3110

First Named Applicant: Henry Wurzburg

Attorney Docket Number: 5707-06000

Art Unit: 2876

Examiner: Unknown Unknown

Search string: (6435904 or 6279060 or 6505267 or 6510524 or 5953511 or 6467042 or 6389544
or 6317839 or 5793359 or 6601180 or 6519669).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
<i>HL</i>	1	6435904	2002-08-20	Herbst et al.			
	2	6279060	2001-08-21	Luke et al.			
	3	6505267	2003-01-07	Luke et al.			
	4	6510524	2003-01-21	Osburn et al.			
	5	5953511	1999-09-14	Sescila, III, et al.			
	6	6467042	2002-10-15	Wright et al.			
	7	6389544	2002-05-14	Katagiri			
	8	6317839	2001-11-13	Wells			
	9	5793359	1998-08-11	Ushikubo			
	10	6601180	2003-07-29	Paredes et al.			
<i>HL</i>	11	6519669	2003-02-11	Paredes et al.			

Signature

Yanagisawa

Examiner Name	Date
<i>[Signature]</i>	<i>4/16/05</i>

KS
11/21/06